

SN 09/672,711

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Thomas E. Burton et al.	Examiner:	Larry Donaghue
Serial No.:	09/672,711	Group Art Unit:	2154
Filed:	September 29, 2000	Docket:	884.984US1
Title:	HOST-FABRIC ADAPTER HAVING HARDWARE ASSIST ARCHITECTURE AND METHOD OF CONNECTING A HOST SYSTEM TO A CHANNEL- BASED SWITCHED FABRIC IN A DATA NETWORK		
Assignee:	Intel Corporation	Customer Number:	21186

INFORMATION DISCLOSURE STATEMENT

MS RCE
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

In compliance with the duty imposed by 37 C.F.R. § 1.56, and in accordance with 37 C.F.R. §§ 1.97 *et. seq.*, the enclosed materials are brought to the attention of the Examiner for consideration in connection with the above-identified patent application. Applicants respectfully request that this Information Disclosure Statement be entered and the documents listed on the attached Form 1449 be considered by the Examiner and made of record. Pursuant to the provisions of MPEP 609, Applicants request that a copy of the 1449 form, initialed as being considered by the Examiner, be returned to the Applicants with the next official communication.

Pursuant to 37 C.F.R. §1.97(b), it is believed that no fee or statement is required with the Information Disclosure Statement.

INFORMATION DISCLOSURE STATEMENT

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Serial No :09/672,711

Filing Date: September 29, 2000

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Assignee: Intel Corporation

Page 2

Dkt: 884.984US1 (INTEL)

The Examiner is invited to contact the Applicants' Representative at the below-listed telephone number if there are any questions regarding this communication.

Pursuant to 37 C.F.R. 1.98(a)(2), Applicant believes that copies of cited U.S. Patents and Published Applications are no longer required to be provided to the Office. Notification of this change was provided in the United States Patent and Trademark Office OG Notices dated October 12, 2004. Thus, Applicant has not included copies of any US Patents or Published Applications cited with this submission. Should the Office require copies to be provided, Applicant respectfully requests that notice of such requirement be directed to Applicant's below-signed representative. Applicant acknowledges the requirement to submit copies of foreign patent documents and non-patent literature in accordance with 37 C.F.R. 1.98(a)(2).

Respectfully submitted,

THOMAS E. BURTON ET AL.

By their Representatives,

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Date FEB. 21/2006


By Robert Madden
Robert Madden
Reg. No. 57,521

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: MS RCE, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 21st day of February, 2006.

Name Amy Moriarty

Signature [Signature]

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB control number.

Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)		Complete if Known	
		Application Number	09/672,711
		Filing Date	September 29, 2000
		First Named Inventor	Burton, Thomas
		Group Art Unit	2154
		Examiner Name	Donaghue, Larry
Sheet 1 of 1		Attorney Docket No: 884.984US1	

US PATENT DOCUMENTS				
Examiner Initial *	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Filing Date If Appropriate
	US-20030091037A1	05/15/2003	Latif, A. , et al.	04/30/2002
	US-6,778,548	08/17/2004	Burton, T. E., et al.	06/26/2000
	US-6,937,611	08/30/2005	Ward, K. A.	04/21/2000

OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		"InfiBand(TM) Architecture Specification", <u>InfiBand Trade Association</u> , Volume 1, Release 1.0, InfiBand Trade Association,(Oct. 24, 2000),1-880	
		"InfiBand(TM) Architecture Specification", <u>InfiBand Trade Association</u> , Volume 2, Release 1.0, InfiBand Trade Association,(Oct. 24, 2000),1-623	
		BEGEL, A. , et al., "An Analysis of VI Architecture Primitives in Support of Parallel and Distributed Communication", <u>Concurrency and Computation: Practice and Experience</u> , 14(1), (2001),1-16	
		BUONADONNA, P. , et al., "An Implementation and Analysis of the Virtual Interface Architecture", <u>IEEE/ACM Conference on Supercomputing, SC98</u> , (1998),1-19	
		DUNNING, D. , et al., "The Virtual Interface Architecture", <u>IEEE Micro</u> , 18(2), (1998),66-76	
		GARCIA, D. , et al., "Future I/O", <u>The 6th International Conference on Parallel Interconnects</u> , 1999. (PI '99) Proceedings., (Oct. 1999),1-5	
		MAHON, M. J., et al., "Hewlett-Packard Precision Architecture: The Processor", <u>Hewlett-Packard Journal</u> , (Aug. 1986),4-22	
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EXAMINER

DATE CONSIDERED

Substitute Disclosure Statement Form (PTO-1449)

* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. 1 Applicant's unique citation designation number (optional) 2 Applicant is to place a check mark here if English language Translation is attached